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(19) **United States**(12) **Patent Application Publication****Lee et al.**(10) **Pub. No.: US 2005/0057146 A1**(43) **Pub. Date: Mar. 17, 2005**(54) **ELECTRO-LUMINESCENCE DISPLAY  
DEVICE AND METHOD OF FABRICATING  
THE SAME**(52) **U.S. Cl. .... 313/504; 313/503; 313/506**(75) **Inventors: Jung Hwan Lee, Daegu (KR); Hyo  
Dae Bae, Daegu (KR)**(57) **ABSTRACT**

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An electro-luminescence display device and a method of fabricating the same capable of saving working hours needed to fabricate the electro-luminescence display device and preventing a waste of materials to improve a production yield of the electro-luminescence display device are disclosed.

(73) **Assignee: LG Electronics Inc., Seoul (KR)**(21) **Appl. No.: 10/901,226**

An electro-luminescence display device includes: a connection film being connected a driving circuit; a pad connected to the connection film in order to supply a driving signal from the driving circuit to a signal line formed on a substrate; a conductive film for connecting the connection film to the pad; and a plurality of dummy pads, each dummy pad including a transparent conductive layer, wherein at least one of the dummy pads is formed to expose the transparent conductive layer in order to test an adhesive level of the connection film and the pad by the conductive film.

(22) **Filed: Jul. 29, 2004**(30) **Foreign Application Priority Data**

Jul. 31, 2003 (KR) ..... P2003-52980

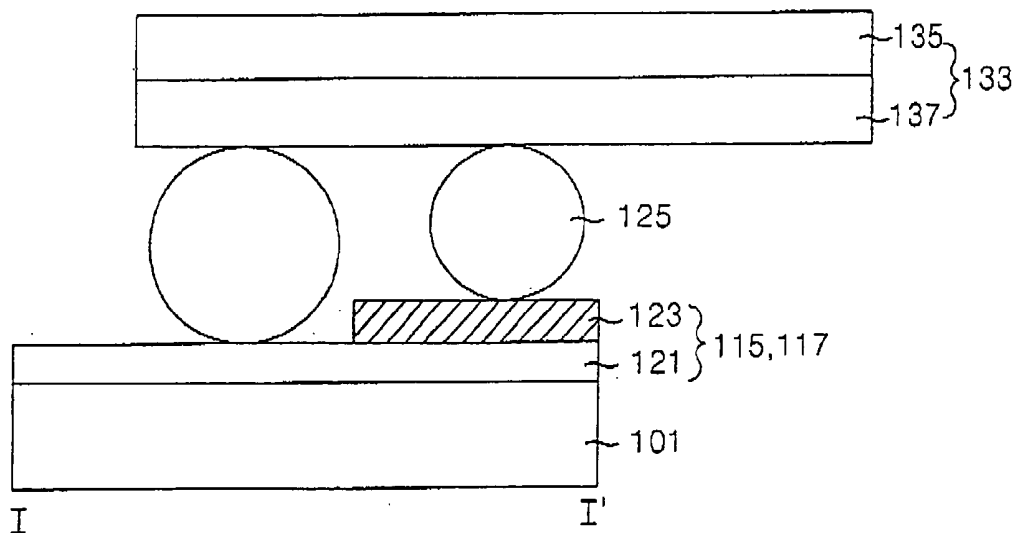
**Publication Classification**(51) **Int. Cl.<sup>7</sup> ..... H05B 33/00**

FIG.1A  
RELATED ART

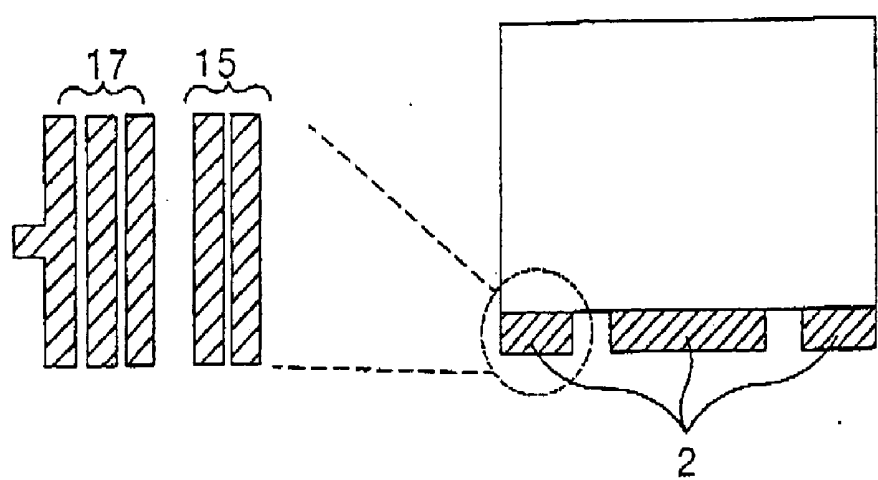


FIG.1B  
RELATED ART

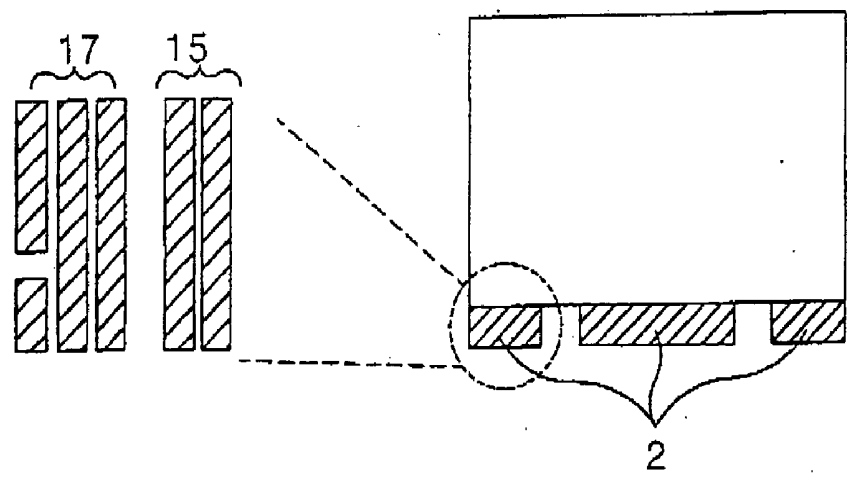


FIG.1C  
RELATED ART

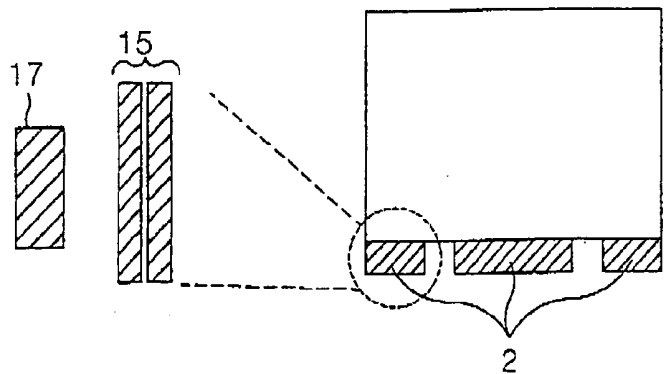


FIG.2  
RELATED ART

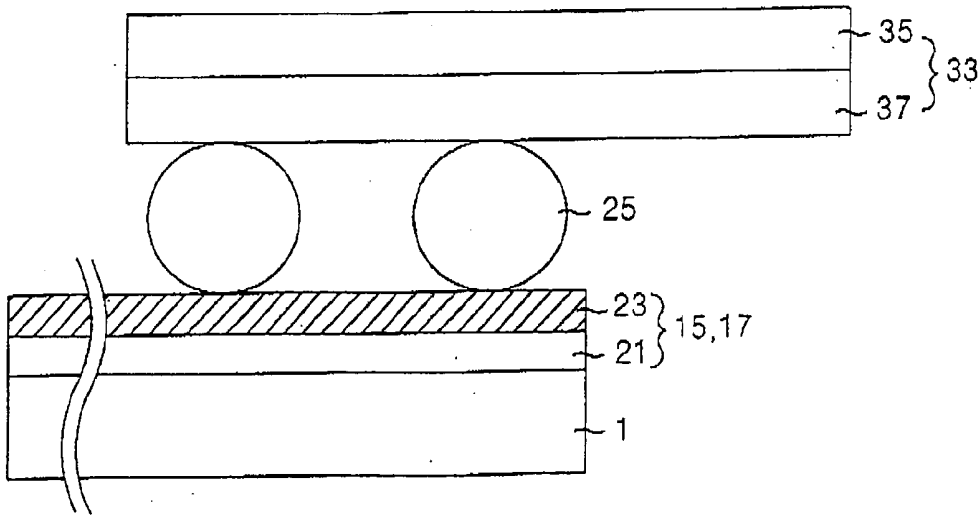


FIG. 3

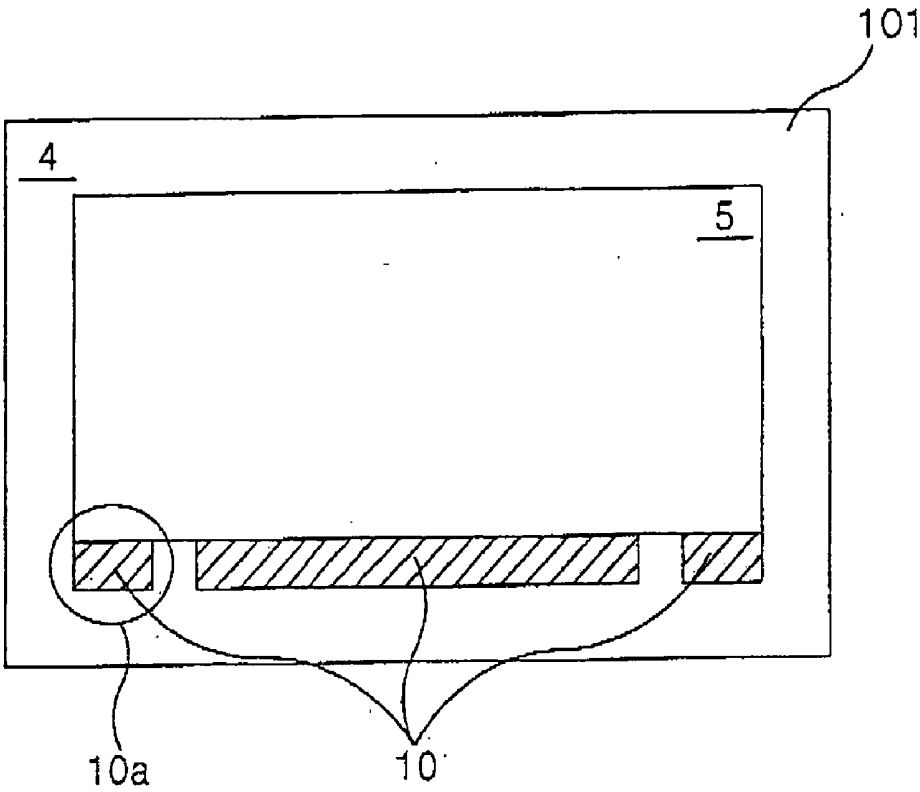


FIG. 4

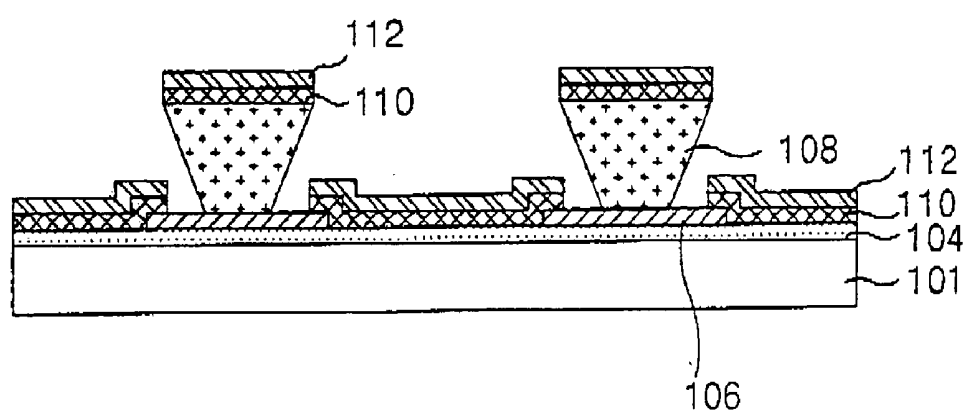


FIG. 5A

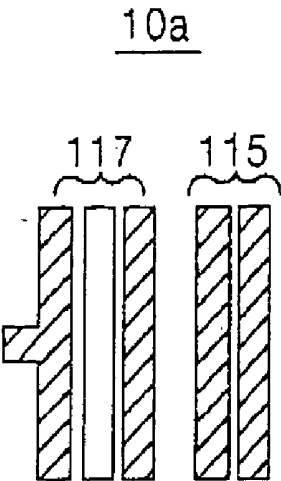


FIG. 5B

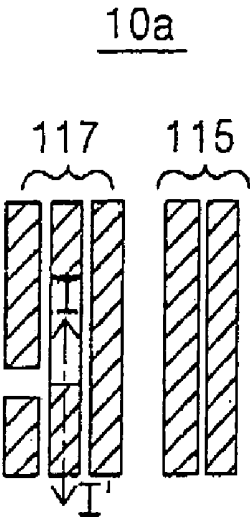


FIG.5C

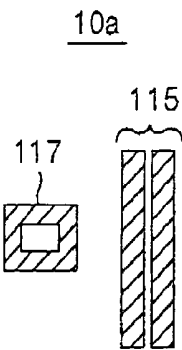
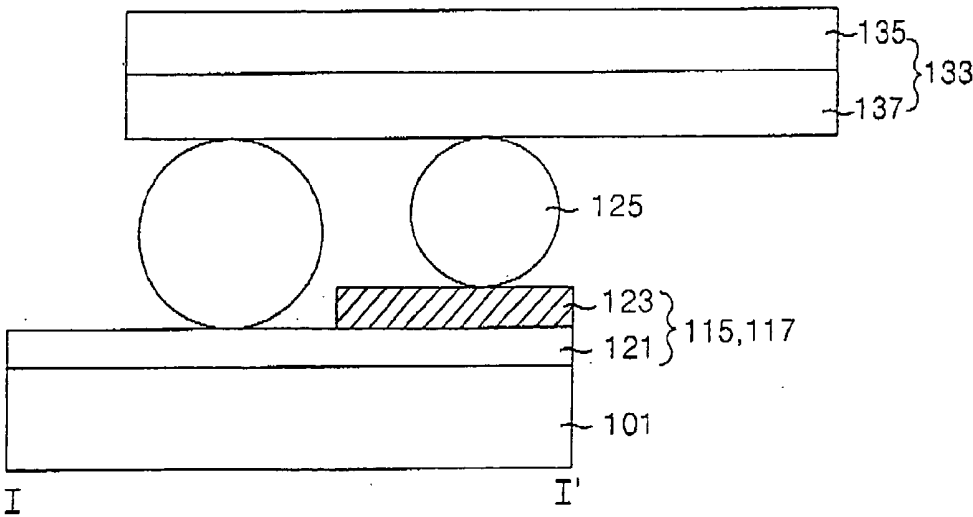


FIG.6



## ELECTRO-LUMINESCENCE DISPLAY DEVICE AND METHOD OF FABRICATING THE SAME

[0001] This application claims the benefit of Korean Patent Application No. P2003-52980 filed in Korea on Jul. 31, 2003, which is hereby incorporated by reference.

### BACKGROUND OF THE INVENTION

#### [0002] 1. Field of the Invention

[0003] The present invention relates to an electro-luminescence display device and, more particularly, to an electro-luminescence display device and a method of fabricating the same capable of saving working hours needed to fabricate the electro-luminescence display device and preventing a waste of materials to improve a production yield of the electro-luminescence display device.

#### [0004] 2. Description of the Related Art

[0005] In recently, there has been developed various flat panel displays with a reduced weight and bulk that are free from the disadvantage of a cathode ray tube CRT. Such flat panel displays include a liquid crystal display LCD, a field emission display FED, a plasma display panel PDP, and an electro luminescence (hereinafter, referred to as an EL) display devices. The structure and fabricating process of the PDP among these is relatively simple. Thus the PDP is most advantageous to be made light and large-sized, but has disadvantages that the light emission efficiency and brightness thereof are low and its power consumption is high. Whereas, the LCD has a disadvantage that the power consumption is high because the LCD is difficult to be made large-sized and adopts a backlight unit. Further, the LCD has a disadvantage that there is a high optical loss caused by optical devices such as a polarizing filter, a prism sheet and a diffusion panel, and its viewing angle is narrow. As compared with the LCD, the EL display device is generally classified into an inorganic EL display device and an organic EL display device. The EL display device has an advantage that its response speed is fast, its light-emission efficiency and brightness are high and it has wide viewing angle. The organic EL display device can display a picture in a high brightness of several ten thousands [ $\text{cd}/\text{m}^2$ ] with a voltage of about 10 [V].

[0006] A related art EL display device includes a display region having a pixel matrix and a non-display region except for the display region, on a substrate.

[0007] On the display region of the EL display device, an anode electrode and a cathode electrode are formed in a direction crossing each other. The anode electrode is separated from its adjacent anode electrodes by a predetermined distance on the substrate. An insulating film having an aperture at every EL cell region is formed on the substrate provided with the anode electrode. A barrier rib is located on the insulating film to separate each cell having the cathode electrode and an organic EL layer to be formed on the insulating film from adjacent cells. The barrier rib is formed in a direction across the anode electrode. The organic EL layer including an organic compound and the cathode electrode are sequentially deposited on an entire surface of the insulating film provided with the barrier rib. The organic EL layer includes a hole carrier layer, a light-emitting layer and an electron carrier layer which are stacked on the insulating film. According to the organic EL display device, when a

driving signal is applied to the anode electrode and the cathode electrode, the electron and the hole are emitted from the anode electrode and the cathode electrode and the emitted electron and hole are re-combined each other in the organic EL layer, to thereby generate a visible light. The generated visible light is exited to an exterior via the anode electrode and is used to display a picture or image.

[0008] In the EL display device, as shown in **FIGS. 1A** to **1C**, a pad part **2** of the non-display region is connected to a connection film such as a flexible print circuit FPC, a tape carrier package TCP and a chip on film COF in order to receive a driving signal from an exterior. Herein, the pad part **2** is connected to connection devices and includes a first pad **15** for receive the driving signal and a second pad **17** for aligning and testing an adhesive strength to the connection film as shown in **FIGS. 1A** to **1C**. Herein, the second pad **17** can be formed in a variety of types, as shown in **FIGS. 1A** to **1C**.

[0009] **FIG. 2** is a sectional view illustrating an adhesion of the pad and the connection film of the driving circuit in a related art EL display device.

[0010] Each of the pads **15** and **17**, as shown in **FIG. 2**, has a structure, in which a transparent conductive layer **21** connected to the anode electrode or the cathode electrode formed at the display region, and an opaque conductive layer **23** for raising a conductivity of the transparent conductive layer **21**.

[0011] If a connection film **33** in which a base film **35** and a copper material **37** are stacked is pressurized with respect to the pads **15** and **17** while positioning an anisotropic conductive film ACF having conductive balls **25** between the connection film **33** and the pads **15** and **17**, then the connection film **33** is electrically connected to the pads **15** and **17** by the conductive ball **25** pressurized between the pads **15** and **17**, and the connection film **33**.

[0012] Meanwhile, a separate destructive testing process is performed in the related art EL display device in order to confirm that the pads **15** and **17**, and the connection film **33** are normally adhered through the use of the conductive film.

[0013] More specifically describing the destructive testing process, an adhesion of the pads **15** and **17**, and the connection film **33** by the conductive film is performed using a parameter such as a predetermined pressure and strength. And then, the second pad **17** and the connection film **33** are separated from the conductive film. At this time, the parameter such as the adhesion level and the pressed strength between the second pad **17** and the connection film **33**, and the conductive film is determined to settle a parameter for an optimum connection. The optimum parameter determined by the destructive testing process is used in the adhesion process to adhere the first pad **15** and the connection film **33** to the conductive film.

[0014] However, if the second pad **17** and the connection film **33**, and the connection film are separated each other by the destructive testing process, then the second pad **17** and the connection film **33** thus separated cannot be used any more. Hereby, working hours needed to fabricate the EL display device becomes long by the number of times that the testing process is being performed. Further, the device subjected to the testing process should be abolished.

## SUMMARY OF THE INVENTION

[0015] Accordingly, it is an object of the present invention to provide an electro-luminescence device and a method of fabricating the same capable of reducing working hours and preventing a waste of materials to improve a production yield by testing an adhesive level of a pad and a connection film of a driving circuit to a conductive film without requiring a separate destructive testing process.

[0016] In order to achieve these and other objects of the invention, an electro-luminescence device according to the present invention includes: a connection film being connected to a driving circuit; a pad connected to the connection film in order to supply a driving signal from the driving circuit to a signal line formed on a substrate;

[0017] a conductive film for connecting the connection film to the pad; and a plurality of dummy pads, each dummy pad including a transparent conductive layer, wherein at least one of the dummy pads is formed to expose the transparent conductive layer in order to test an adhesive level of the connection film and the pad by the conductive film.

[0018] Each of the dummy pads includes: an opaque conductive layer formed on the transparent conductive layer.

[0019] At least one of the dummy pads includes an alignment mark for aligning the connection film and the pad.

[0020] Any one of the dummy pads has a single-layer structure of the transparent conductive layer, and the remaining dummy pads have a double-layer structure of the transparent conductive layer and the opaque conductive layer.

[0021] The opaque conductive layer exposes at least a portion of the transparent conductive layer.

[0022] A method of manufacturing an electro-luminescence device according to the present invention includes: forming a dummy pad having a transparent conductive layer, a portion of transparent conductive layer being exposed; adhering the dummy pad and a connection film using a conductive film, the connection film being connected to a driving circuit; and testing an adhesive strength of the dummy pad and the connection film by the conductive film.

## BRIEF DESCRIPTION OF THE DRAWINGS

[0023] These and other objects of the invention will be apparent from the following detailed description of the embodiments of the present invention with reference to the accompanying drawings, in which:

[0024] FIGS. 1A to 1C, are configurations illustrating a portion of a pad part of an electro-luminescence device;

[0025] FIG. 2 is a configuration illustrating an adhesion of a connection film of a driving circuit and the pad shown in FIG. 1;

[0026] FIG. 3 is a configuration illustrating an electro-luminescence device according to an embodiment of the present invention;

[0027] FIG. 4 is a sectional view illustrating a portion of a display region of the electro-luminescence device shown in FIG. 3;

[0028] FIGS. 5A to 5C are configurations illustrating a portion of a pad part of the electro-luminescence device according to the embodiment of the present invention; and

[0029] FIG. 6 is a configuration illustrating an adhesion of a connection film of a driving circuit and a second pad of the electro-luminescence device according to the embodiment of the present invention.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

[0030] Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

[0031] Hereinafter, the preferred embodiments of the present invention will be described in detail with reference to FIGS. 3 to 6.

[0032] FIG. 3 is a schematic diagram of an electro-luminescence (EL) device according to an embodiment of the present invention.

[0033] The EL display device shown in FIG. 3 includes a display region 5 having a pixel matrix and a non-display region 4 having a pad connected to signal lines of the display region 5, on a substrate 101.

[0034] On the display region 5 of the EL display device, as shown in FIG. 4, an anode electrode 104 and a cathode electrode 112 are formed in a direction crossing each other. The anode electrode 104 is separated from its adjacent anode electrodes by a predetermined distance on the substrate 101. An insulating film 106 having an aperture at every EL cell region is formed on the substrate 101 provided with the anode electrode 104. A barrier rib is located on the insulating film to separate each cell having the cathode electrode 112 and an organic EL layer 110 to be formed on the insulating film 106 from adjacent cells. The barrier rib 108 is formed in a direction across the anode electrode 104. The organic EL layer 110 including an organic compound, and the cathode electrode 112 are sequentially deposited on an entire surface of the insulating film 106 provided with the barrier rib 108. The organic EL layer 110 includes a hole carrier layer, a light-emitting layer and an electron carrier layer which are stacked on the insulating film 106. According to the organic EL display device, when a driving signal is applied to the anode electrode 104 and the cathode electrode 112, the electron and the hole are emitted from the anode electrode 104 and the cathode electrode 112, and the emitted electron and hole are recombined each other in the organic EL layer 110, to thereby generate visible light. At this time, the generated visible light is exited to an exterior via the anode electrode 104 and is used to display a picture or image.

[0035] In the EL display device, a pad part 10 of the non-display region 4 is connected to a connection film such as a flexible print circuit FPC, a tape carrier package TCP and a chip on film COF in order to receive a driving signal from an exterior.

[0036] FIGS. 5A to 5C are detailed configurations of a portion 10a of the pad part 10, and FIG. 6 is a sectional view illustrating an adhesion of a connection film 133 of a driving circuit and a second pad 117 shown in FIGS. 5A to 5C.

[0037] Referring to FIGS. 5A to 6, the pad part 10 includes a first pad 115 for receiving the driving signal and

a second pad **117** for aligning and testing an adhesive strength with the connection film. The second pad **117** can be formed in a variety of types for a purpose of aligning the pad part and the connection film, as shown in **FIGS. 5A** to **5C**. For example, the second pad **117** has a protrusion as shown in **FIG. 5A**, a hole used for an aligning mark as shown in **FIG. 5B**, and a separate rectangular shape as shown in **FIG. 5C**.

[0038] Each of the pads **115** and **117** has a structure in which a transparent conductive layer **121** extending from the anode electrode or the cathode electrode formed at the display region **5**, and an opaque conductive layer **123** for raising the conductivity of the transparent conductive layer **121**. Meanwhile, at least one of second pads is formed to expose a portion of the transparent conductive layer **121** or to expose an entire surface of the transparent conductive layer **121**, as shown in **FIG. 6**.

[0039] If a connection film **133** in which a base film **135** and a copper material **137** are stacked is pressurized with respect to the pads **115** and **117** while positioning an anisotropic conductive film ACF having a conductive ball **125** between the connection film **133** and the pads **115** and **117**, then the connection film **133** is electrically connected to the pads **115** and **117** by the conductive ball **125** pressurized between the pads **115** and **117**, and the connection film **133**.

[0040] In order to confirm that the pads **115** and **117**, and the connection film **133** are normally and securely adhered through the use of the conductive film, a pressed strength of the conductive ball **125** is observed through an area from which the opaque conductive layer **123** of the second pad **117** is removed. Accordingly, it is possible to determine whether an adhesion of the pads **115** and **117**, and the connection film **133** to the conductive film is securely made or not. More specifically, the conductive ball **125** is observed through a rear surface of the substrate **101** by use of a microscope. At this time, an adhesive level can be determined depending upon the pressed strength of the conductive ball **125**. In this case, if the conductive ball **125** is seen as a small point through the microscope, then the pressed strength of the conductive ball **125** is regarded as low. Thus, it is determined that the adhesive level of the pads **115** and **117**, and the connection film **133** by the conductive film is weak. On the other hand, if the conductive ball **125** is seen as a relatively large point through the microscope, then the pressed strength of the conductive ball **125** is regarded as relatively high. Thus, it is determined that the adhesive level of the pads **115** and **117**, and the connection film **133** by the conductive film is strong.

[0041] As described above, according to the EL display device of the present invention, the opaque conductive layer **123** in the second pad **117** is made to expose a portion or the entire transparent conductive layer **121** by removing a portion or an entire of the opaque conductive layer **123**. Therefore, it is possible to determine the adhesive level of the pads **115** and **117**, and the connection film **133** by observing the pressed strength of the conductive ball **125** through the removed opaque conductive layer. Accordingly, there is no needed a destructive process, that is, a process for separating the second pad **117**, from the connection film **133**

in order to test the adhesive strength of the pads **115** and **117**, and the connection film **133** by the conductive film in accordance with the related art, so that working hours needed to do is reduced and, in addition, the device is prevented from being abolished by the related art testing process, which results in improving a yield of the EL display device.

[0042] Although the present invention has been explained by the embodiments shown in the drawings described above, it should be understood to the ordinary skilled person in the art that the invention is not limited to the embodiments, but rather that various changes or modifications thereof are possible without departing from the spirit of the invention. Accordingly, the scope of the invention shall be determined only by the appended claims and their equivalents.

What is claimed is:

1. An electro-luminescence display device comprising:

a connection film being connected a driving circuit;

a pad connected to the connection film in order to supply a driving signal from the driving circuit to a signal line formed on a substrate;

a conductive film for connecting the connection film to the pad; and

a plurality of dummy pads, each dummy pad including a transparent conductive layer,

wherein at least one of the dummy pads is formed to expose the transparent conductive layer in order to test an adhesive level of the connection film and the pad by the conductive film.

2. The device according to claim 1, wherein each of the dummy pads includes:

an opaque conductive layer formed on the transparent conductive layer.

3. The device according to claim 1, wherein at least one of the dummy pads includes an alignment mark for aligning the connection film and the pad.

4. The device according to claim 2, wherein any one of the dummy pads has a single-layer structure of the transparent conductive layer, and the remaining dummy pads have a double-layer structure of the transparent conductive layer and the opaque conductive layer.

5. The device according to claim 2, wherein the opaque conductive layer exposes at least a portion of the transparent conductive layer.

6. A method of manufacturing an electro-luminescence device comprising:

forming a dummy pad having a transparent conductive layer, a portion of transparent conductive layer being exposed;

adhering the dummy pad and a connection film using a conductive film, the connection film being connected to a driving circuit; and

testing an adhesive strength of the dummy pad and the connection film by the conductive film.

\* \* \* \* \*

|               |                                                 |         |            |
|---------------|-------------------------------------------------|---------|------------|
| 专利名称(译)       | 电致发光显示装置及其制造方法                                  |         |            |
| 公开(公告)号       | <a href="#">US20050057146A1</a>                 | 公开(公告)日 | 2005-03-17 |
| 申请号           | US10/901226                                     | 申请日     | 2004-07-29 |
| 申请(专利权)人(译)   | LG电子株式会社.                                       |         |            |
| 当前申请(专利权)人(译) | LG DISPLAY CO. , LTD.                           |         |            |
| [标]发明人        | LEE JUNG HWAN<br>BAE HYO DAE                    |         |            |
| 发明人           | LEE, JUNG HWAN<br>BAE, HYO DAE                  |         |            |
| IPC分类号        | H05B33/10 H01L27/32 H05B33/00                   |         |            |
| CPC分类号        | H01L27/3288 H01L27/3223                         |         |            |
| 优先权           | 1020030052980 2003-07-31 KR                     |         |            |
| 其他公开文献        | US7294963                                       |         |            |
| 外部链接          | <a href="#">Espacenet</a> <a href="#">USPTO</a> |         |            |

#### 摘要(译)

本发明公开了一种电致发光显示装置及其制造方法，其能够节省制造电致发光显示装置所需的工作时间并防止材料浪费以提高电致发光显示装置的产量。一种电致发光显示装置，包括：连接膜，连接驱动电路;焊盘连接到连接膜，以便从驱动电路向形成在基板上的信号线提供驱动信号;用于将连接膜连接到焊盘的导电膜;多个虚设焊盘，每个虚设焊盘包括透明导电层，其中形成至少一个虚设焊盘以暴露透明导电层，以通过导电膜测试连接膜和焊盘的粘合水平。

